Amendments to the Claims:

This listing of claims will replace all prior version, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A method comprising:

causing a processor to obtain a random number from an integrated circuit;

receiving a processor identifier;

receiving a seed stored in a non-volatile memory; ~~and~~

hashing said random number, said processor identifier and said seed to develop a device key; and

generating a certificate.

2. (canceled)

3. (currently amended) The method of ~~claim 2 including~~ claim 1, wherein the operation of receiving a processor identifier comprises ~~causing a processor to execute instructions to obtain~~ obtaining a processor serial number.

4. (original) The method of claim 1 including obtaining said processor identifier by executing instructions at the operating system kernel level.

5. (canceled)

6. (currently amended) The method of ~~claim 5~~ claim 1 including causing the processor to send said certificate to said integrated circuit.

7. (currently amended) The method of ~~claim~~ claim 1 including causing said integrated circuit to validate said certificate and process said certificate to generate a new device key.

8. (currently amended) The method of claim 7 including encrypting [[a]] the new device key using a current device key and ~~writing~~ sending the encrypted new device key back to the processor.

9. (currently amended) The method of claim 7 including writing said new device key into a memory in said integrated circuit.

10. (currently amended) ~~The method of claim 1 including~~ A method comprising:

receiving a processor identifier;

receiving a seed stored in a non-volatile memory;

hashing said identifier and said seed to develop a device key;

sending said device key to a head end; and

receiving a digital television broadcast from [[a]] said head end ~~and sending said device key to said head end~~.

11. (currently amended) An article comprising a medium storing instructions that enable a processor-based system to:

cause a processor to obtain a random number from an integrated circuit;

receive a processor identifier;

receive a seed stored in a non-volatile memory; ~~and~~

hash said random number, said processor identifier and said seed to develop a device key; and

generate a certificate.

12. (canceled)

13. (currently amended) The article of claim ~~12 further storing~~ 11, wherein the instructions that enable the processor-based system to receive the processor identifier comprise ~~execute~~ instructions to obtain a processor serial number.

14. (original) The article of claim 11 further storing instructions that enable the processor-based system to obtain said processor identifier by executing instructions at ring 0.

15. (canceled)

16. (currently amended) The article of ~~claim 15~~ claim 11 further storing instructions that enable the processor-based system to send said certificate to said integrated circuit.

17. (currently amended) The article of ~~claim 16~~ claim 11 further storing instructions that enable the processor-based system to cause said integrated circuit to validate said certificate and process said certificate to generate a new device key.

18. (currently amended) The article of claim 17 further storing instructions that enable the processor-based system to encrypt [[a]] the new device key using a current device key.

19. (currently amended) The article of claim 17 further storing instructions that enable the processor-based system to write said new device key into a memory in said integrated circuit.

20. (currently amended) ~~The article of claim 11 further~~ An article comprising a medium storing instructions that enable ~~the~~ a processor-based system [[to]] to:

receive a processor identifier;

receive a seed stored in a non-volatile memory;

hash said identifier and said seed to develop a device key;

send said device key to a head end; and

receive a digital television broadcast from the head end ~~and send said device key to said head end~~.

21. (currently amended) An integrated circuit comprising:

an interface to couple said circuit to a processor-based system;

a transport demultiplexer coupled to said interface to receive audio/video content information;

a key logic circuit to extract a device key from a bit stream including a processor serial number and a device key seed; ~~and~~

a memory to store said device key; and

a bus that couples said interface, said transport demultiplexer and said key logic circuit.

22. (original) The circuit of claim 21 wherein said memory is part of said transport demultiplexer.

23. (canceled)

24. (original) The circuit of claim 21 wherein said key logic circuit generates a random challenge on request from said processor-based system.

25. (currently amended) The circuit of claim 21 wherein said key logic circuit receives a certificate from said processor-based system and processes said certificate to generate a new device key.

26. (currently amended) The circuit of claim 25 wherein said key logic encrypts [[a]] the new device key using a current device key.

27. (currently amended) A processor-based system comprising:

a processor that stores instructions that enable said processor to obtain a processor serial number;

a non-volatile memory, coupled to said processor, to store a device key seed; and

an integrated circuit coupled to said processor, said integrated circuit including a key logic circuit that generates a random challenge upon request from said ~~processor~~ processor;

wherein said key logic circuit extracts a device key from a bit stream including the processor serial number and the device key seed.

28. (canceled)

29. (currently amended) The system of ~~claim 28~~ claim 27 including a memory in said integrated circuit, said key logic circuit enabling said device key to be stored in said memory.

30. (original) The system of claim 29 wherein said integrated circuit includes a transport demultiplexer that receives content from an external source, said memory being included as part of said transport demultiplexer.